PUB4110 (PU4110), **PUB4410** (PU4410)

Silicon NPN triple diffusion planar type

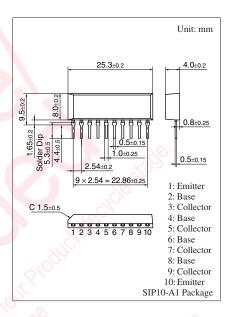
For power amplification/switching Complementary to PUB4210 (PU4210), PUB4510 (PU4510)

■ Features

- \bullet High forward current transfer ratio $h_{F\!E}$ which has satisfactory linearity
- ullet Low collector-emitter saturation voltage $V_{CE(sat)}$
- PUB4110 (PU4110): NPN 4 elements PUB4410 (PU4410): NPN 2 elements × 2

■ Absolute Maximum Ratings $T_C = 25$ °C

Parameter	Symbol	Symbol Rating		
Collector-base voltage (Emitter open)	V _{CBO}	60	V	
Collector-emitter voltage (Base open)	V _{CEO}	60	V	
Emitter-base voltage (Collector open)	V _{EBO}	6	V	
Collector current	I_{C}	3	A	
Peak collector current	I_{CP}	5	A	
Base current	I_{B}	1	A	
Collector power dissipation	P _C	15	W	
$T_a = 25$ °C		3.5		
Junction temperature	Tj	150	°C	
Storage temperature	T_{stg}	-55 to +150	°C	

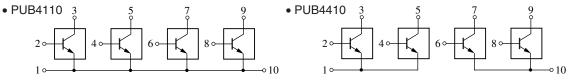


■ Electrical Characteristics $T_C = 25^{\circ}C \pm 3^{\circ}C$

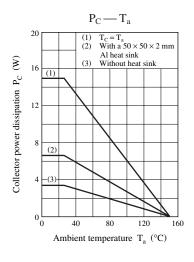
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-emitter voltage (Base open)	V _{CEO}	$I_C = 30 \text{ mA}, I_B = 0$	60	dille		V
Base-emitter voltage	V _{BE}	$V_{CE} = 4 \text{ V}, I_{C} = 3 \text{ A}$	10	5	1.8	V
Collector-emitter current (E-B short)	I _{CES}	$V_{CE} = 60 \text{ V}, V_{BE} = 0$	20/		200	μΑ
Collector-emitter cutoff current (Base open)	I_{CEO}	$V_{CE} = 30 \text{ V}, I_{B} = 0$	7.7		300	μΑ
Emitter-base cutoff current (Collector open)	I_{EBO}	$V_{EB} = 6 \text{ V}, I_{C} = 0$			1	mA
Forward current transfer ratio	h _{FE1}	V _{CE} = 4 V, I _C = 1 A	70		250	_
	h _{FE2}	$V_{CE} = 4 \text{ V}, I_{C} = 3 \text{ A}$	10			
Collector-emitter saturation voltage	V _{CE(sat)}	$I_C = 3 \text{ A}, I_B = 0.375 \text{ A}$			1.2	V
Transition frequency	f_T	V _{CE} = 10 V, I _C = 0.5 A, f = 10 MHz		30		MHz
Turn-on time	t _{on}	$I_C = 1 A$		0.5		μs
Storage time	t _{stg}	$I_{B1} = 0.1 \text{ A}, I_{B2} = -0.1 \text{ A}$		2.5		μs
Fall time	$t_{\rm f}$	$V_{CC} = 50 \text{ V}$		0.4		μs

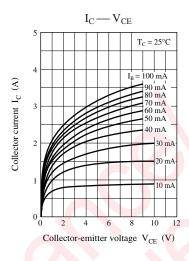
Note) Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

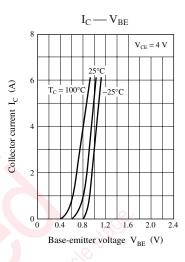
Internal Connection

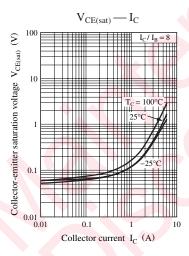


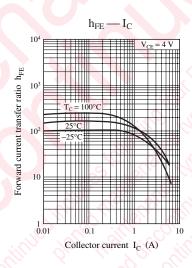
Note) The part numbers in the parenthesis show conventional part number.

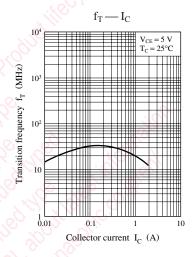


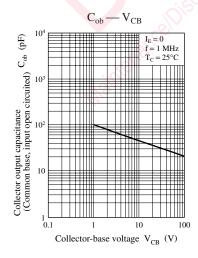


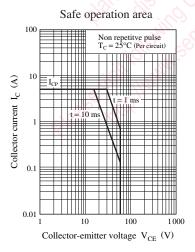












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